

Taeg Sang (Tim) Cho Ph.D.

Partner, New York



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Taeg Sang (Tim) Cho has over a decade of experience in handling complex patent disputes. Dr. Cho's practice focuses on contested matters before the Patent Trial and Appeal Board (PTAB), including *inter partes* review (IPR), post-grant review (PGR), and reexamination proceedings. He has experience in all phases of PTAB proceedings, including drafting petitions and briefs; taking and defending expert depositions; and participating in oral hearings before the PTAB. He has served as lead counsel in several IPR proceedings that successfully invalidated patents asserted in concurrent district court litigations.

Dr. Cho has also successfully litigated patent cases in federal district courts. He has handled all pre-trial phases of litigation—including pre-suit investigations, claim construction, fact and expert discovery, depositions, dispositive motions, and trial preparations—in cases involving a wide range of complex technologies such as semiconductor devices, analog and digital electronics, computer software, database systems, signal processing systems, and wireless communications standards and systems.

Dr. Cho has particular expertise in artificial intelligence and machine learning. Before starting his legal career, Dr. Cho received his doctorate degree from the Massachusetts Institute of Technology for his work on machine learning algorithms and imaging electronics. For his research, he received a paper award at one of the premier conferences in machine learning, IEEE Conference on Computer Vision and Pattern Recognition (CVPR). Dr. Cho has served as a paper reviewer for the machine learning community, including for IEEE CVPR, IEEE ICCV, ECCV and AAAI. Dr. Cho is the lead author of eleven research articles and a co-inventor of three U.S. patents.

Prior Experience

- Counsel, Wilmer Cutler Pickering Hale and Dorr LLP, New York, NY

Clerkship

- Law Clerk to the Hon. Paul A. Crotty, United States District Court for the Southern District of New York, 2017-2018

EDUCATION

Fordham University School of Law,
J.D., *magna cum laude*; Order of
the Coif

Massachusetts Institute of
Technology, PhD, Electrical
Engineering and Computer
Science

Massachusetts Institute of
Technology, SM, Electrical
Engineering and Computer Science

Korea Advanced Institute of
Science and Technology (KAIST),
BS, Electrical Engineering,
summa cum laude

ADMISSIONS

2017, New York

United States Patent and
Trademark Office

Courts

- United States Court of Appeals for the Federal Circuit
- United States District Court for the Southern District of New York
- United States District Court for the Eastern District of New York

Memberships and Affiliations

- PTAB Bar Association
- Federal Circuit Bar Association

Other Distinctions

- New York Metro Rising Star, Super Lawyers, 2020-2021

Representative Matters

- Represented national bank in inter partes review proceedings and district court litigation involving remote check deposit.
- Represented leading microelectronics manufacturer in inter partes review proceedings involving semiconductor devices, semiconductor manufacturing processes, and computer architecture.
- Represented leading microelectronics manufacturer in patent infringement suits involving computer architecture, semiconductor devices, and memory circuits.
- Represented leading telecommunications company in patent infringement suits involving signal processing systems.
- Counseled leading microelectronics manufacturer on pre-suit investigations and patent assertion strategy.

* Attorney experience includes matters before joining Desmarais LLP.